

REMARKS

Claims 1 - 24 are cancelled. Claims 25 – 47 are pending. Claim 25 is amended with this paper.

Specification

In accordance with 35 U.S.C. §119(e) and 35 U.S.C. §120, the Applicants have submitted an amendment containing a specific reference to the prior filed applications in the section above entitled “In the Specification”.

Claim Rejections – 35 USC § 102

Claims 25-47 are rejected under 35 U.S.C. 102(e) as being unpatentable over Shipton *et al.* (Pub. No. 2006/0052962)

Applicant has amended independent Claim 25 to clarify that the complementary error signals are sourced from respective independent processor cores. Such a structure enables each processor core to cross check its computational results against the other. As best understood Shipton *et al.* does not provide an error checking circuit with multiple independent processor cores. Applicant therefore believes that Shipton *et al.* does not anticipate amended independent Claim 25.

The remaining Claims depend either directly or indirectly from Claim 25 and are therefore believed to be in a condition for allowance for at least the same reason as Claim 25.

CONCLUSION

Accordingly, Applicant believes that the claims as amended overcome the raised objections and rejections and are in a condition for allowance.

Respectfully submitted,

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